CR9-98-027B P.M. LiVecchi Performance Enhancements for Threaded Servers 1 of 9

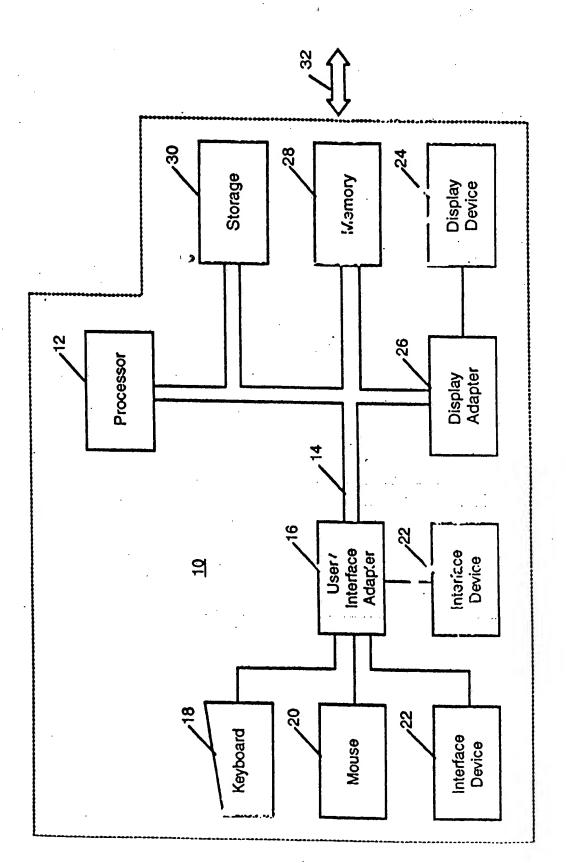
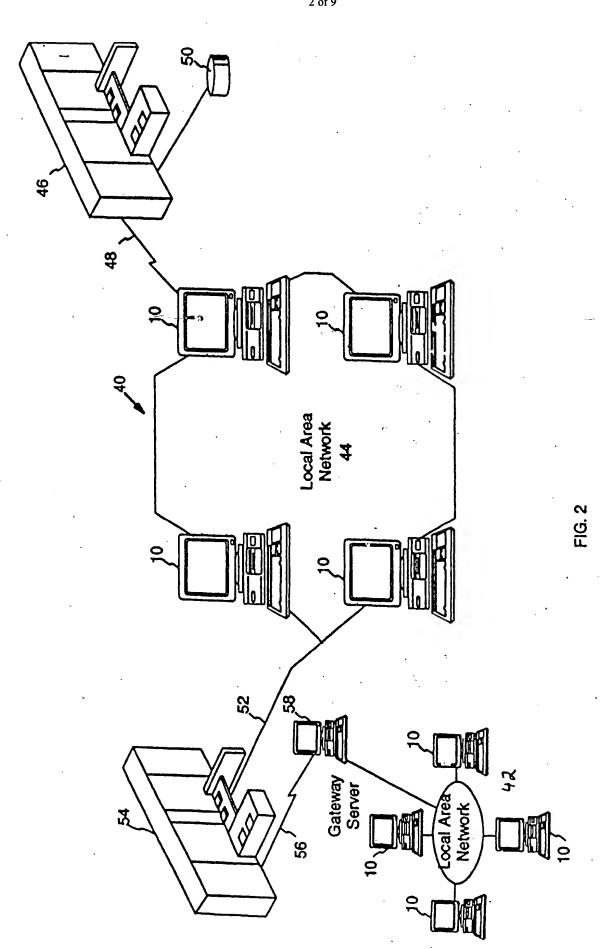
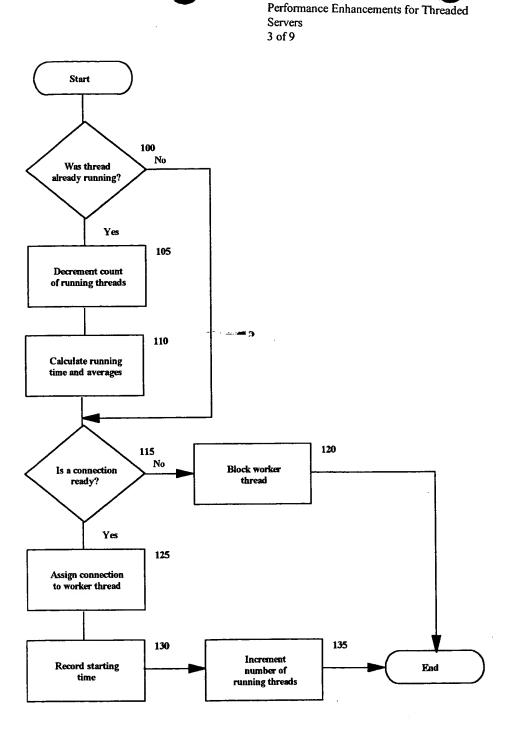


FIG. 1





CR9-98-027B

Figure 3A

,

CR9-98-027B
Performance Enhance
Servers
4 of 9

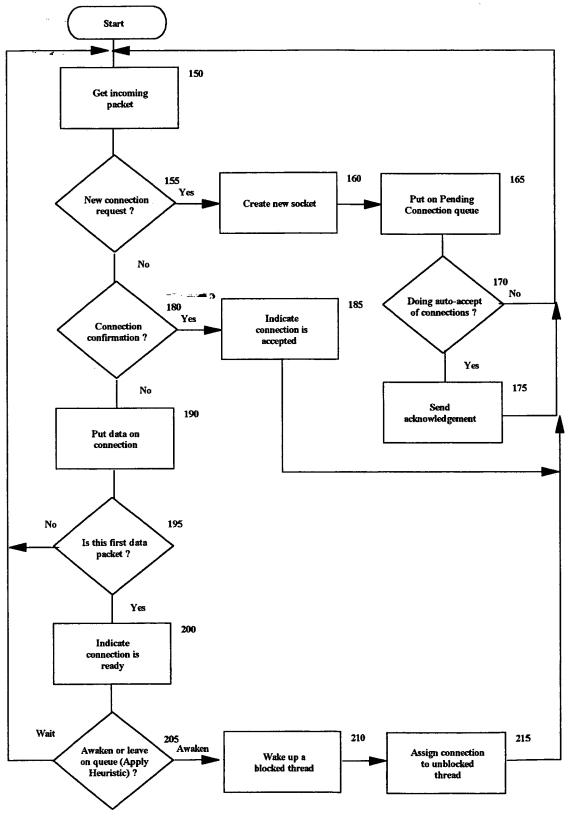
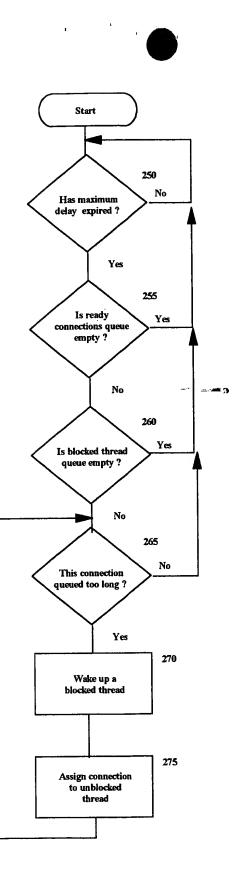


Figure 3B



CR9-98-027B
Performance Enhanceme
Servers
5 of 9

Figure 3C

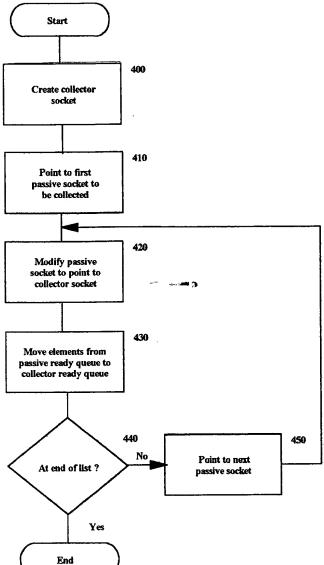


Figure 4

Passive Socket

Figure 5A

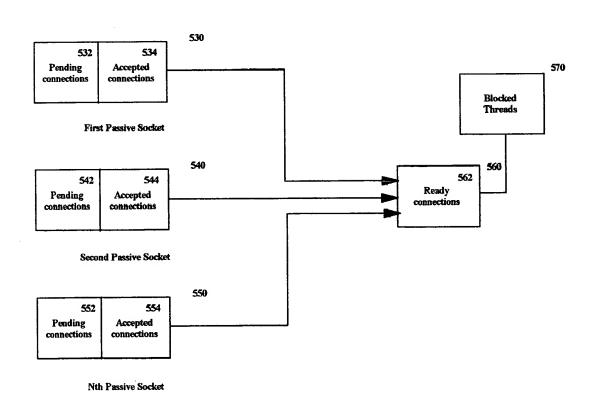
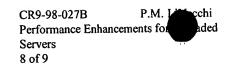


Figure 5B



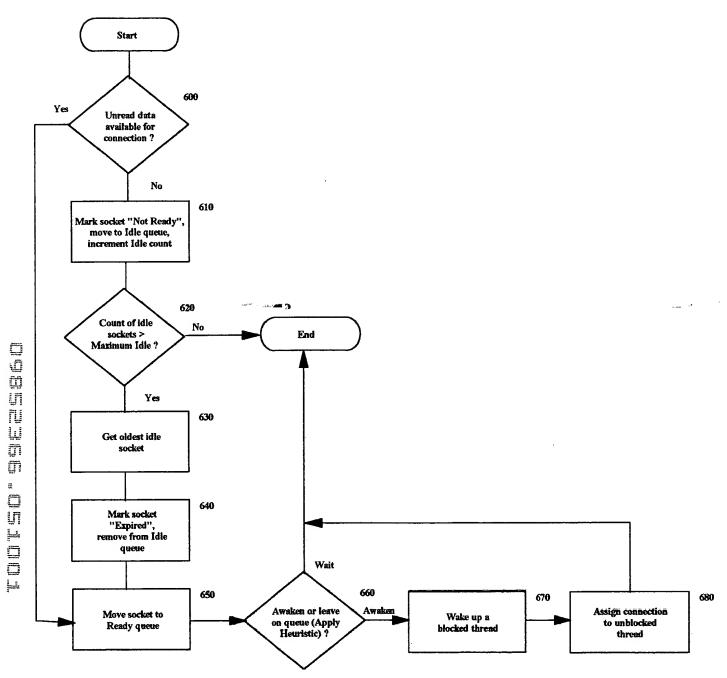
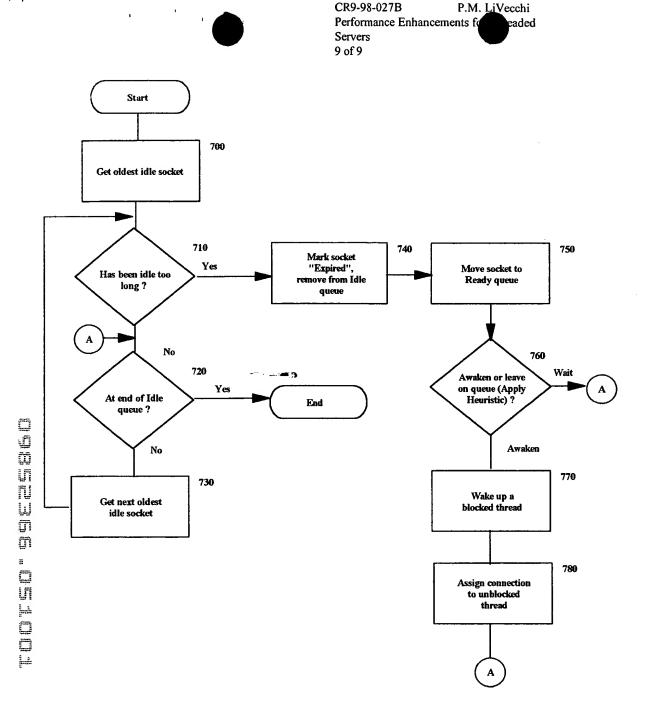


Figure 6A



CR9-98-027B

Figure 6B